Schottky Barrier Diode

Schottky barrier diodes are designed primarily for high-efficiency UHF and VHF detector applications. Readily available to many other fast switching RF and digital applications.

- Very Low Capacitance Less than 1.0 pF @ Zero Volts
- Low Noise Figure 6.0 dB Typ @ 1.0 GHz
- Device Marking: 4M

MAXIMUM RATINGS

Symbol	Rating	Value	Unit
٧ _R	Reverse Voltage	7.0	Vdc

THERMAL CHARACTERISTICS

Symbol	Characteristic		Unit
PD	Total Device Dissipation FR-5 Board,* TA = 25°C	200	mW
	Derate above 25°C	1.57	mW/°C
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	635	°C/W
T _J , T _{stg}	Junction and Storage Temperature Range	-55 to +150	°C

^{*}FR-5 Minimum Pad



ON Semiconductor™

http://onsemi.com

1.0 pF SCHOTTKY BARRIER DIODE



PLASTIC SOD-323 CASE 477



ORDERING INFORMATION

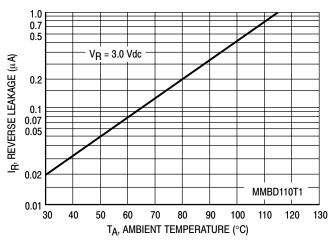
Device	Package	Shipping
MMDL101T1	SOD-323	3000 / Tape & Reel

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA)	V(BR)R	7.0	10	_	Volts
Diode Capacitance (V _R = 0, f = 1.0 MHZ, Note 1)	СТ	_	0.88	1.0	pF
Reverse Leakage (V _R = 3.0 V)	IR	_	20	250	nAdc
Noise Figure (f = 1.0 GHz, Note 2)	NF	_	6.0	_	dB
Forward Voltage (I _F = 10 mA)	VF	_	0.5	0.6	Vdc

^{*}Notes on Next Page

TYPICAL CHARACTERISTICS



100 TA = 85°C

TA = -40°C

TA = 25°C

MMBD110T1

O.1

O.3

O.4

O.5

O.6

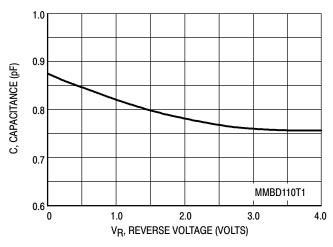
O.7

O.8

VF, FORWARD VOLTAGE (VOLTS)

Figure 1. Reverse Leakage

Figure 2. Forward Voltage





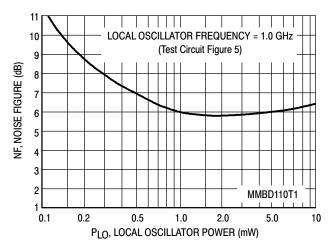


Figure 4. Noise Figure

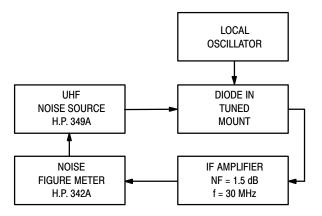


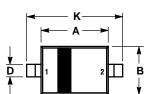
Figure 5. Noise Figure Test Circuit

NOTES ON TESTING AND SPECIFICATIONS

Note 1 — C_C and C_T are measured using a capacitance bridge (Boonton Electronics Model 75A or equivalent).

Note 2 — Noise figure measured with diode under test in tuned diode mount using UHF noise source and local oscillator (LO) frequency of 1.0 GHz. The LO power is adjusted for 1.0 mW. IF amplifier NF = 1.5 dB, f = 30 MHz, see Figure 5.

PACKAGE DIMENSIONS



NOTE 3

SOD-323 PLASTIC PACKAGE CASE 477-02 **ISSUE A**



- NOTES:

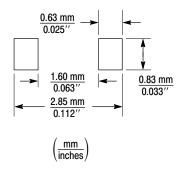
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: MILLIMETERS.

 3. LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	1.60	1.80	0.063	0.071	
В	1.15	1.35	0.045	0.053	
С	0.80	1.00	0.031	0.039	
D	0.25	0.40	0.010	0.016	
Е	0.15 REF		0.006 REF		
Н	0.00	0.10	0.000	0.004	
7	0.089	0.177	0.0035	0.0070	
K	2.30	2.70	0.091	0.106	

STYLE 1: PIN 1. CATHODE 2. ANODE



SOD-323 Soldering Footprint

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